

In the claims, kindly amend as follows:

1. (original) A processor resetting apparatus comprising:  
a fibre channel arbitrated loop (FC-AL) interface arranged to receive a frame containing an indicator of a reset command for a server including a processor associated with said resetting apparatus; and  
reset means, responsive to said reset command, to issue a reset command for resetting said processor.
2. (presently amended) The apparatus of ~~Apparatus as claimed in~~ claim 1 wherein the server is one of a redundant pair of servers.
3. (presently amended) The apparatus of ~~Apparatus as claimed in~~ claim 1 wherein the apparatus comprises one of a separate component of a server motherboard or an integral element of a server motherboard.
4. (presently amended) The apparatus of ~~Apparatus as claimed in~~ claim 1 wherein said FC-AL interface is arranged to receive a frame indicative of a lock request for a resource and wherein said apparatus further comprises:  
means for receiving from said associated processor an indicator of a resource to be locked;  
means for causing a corresponding indicator to be stored;  
means for causing said stored indicator to be deleted when an associated resource is unlocked;  
means, responsive to receiving a lock request frame originating from another processor, for checking any stored indicators for a matching locked resource;  
means, responsive to detecting a match, for transmitting a frame indicative of said resource being locked by said processor to the originator of said lock request; and  
means, responsive to not detecting a match, for transmitting said lock request frame to the originator of said lock request.
5. (new) A processor resetting apparatus comprising:  
  
a fibre channel arbitrated loop (FC-AL) interface arranged to receive a frame containing

an indicator of a reset command for a server including a processor associated with said resetting apparatus; and

a reset controller, responsive to said reset command, to issue a reset interrupt command for resetting said processor.

6. (new) A method for use with a system comprising first and second servers communicatively coupled over a fibre channel arbitrated loop (FC-AL) communications channel, each server comprising an FC-AL interface coupled to the FC-AL communications channel, and arranged to receive a frame containing an indicator of a reset command for a server including a processor associated with said resetting apparatus; and a reset controller, responsive to said reset command, to issue a reset interrupt command for resetting said processor; the method comprising the steps of:

at the first server, sending a frame over the FC-AL communications channel containing an indicator of a reset command addressed to the second server,

at the second server, receiving the frame over the FC-AL communications channel containing the indicator of the reset command addressed to the second server;

at the second server, in response to the receipt of the frame containing the indicator of the reset command, issuing a reset interrupt command to the processor of the second server;

whereby the processor of the second server is reset.